



Mahadevananda Mahavidyalaya

Accredited by NAAC - A Grade

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Ref No.....

Date 1/6/18


Notice No. 277/2017-18 (Contd.)

Department-wise sealed quotations are invited from vendors for purchase of equipment from grant under RUSA 2.0 scheme.

Quotations are to be submitted before 2:00 PM, June 7, 2018.

5. Department of Electronics

Sl. #	Name of the equipment with specification	Quantity
1	Characteristics of a series RC circuit	1
2	To determine an unknown low resistance using potentiometer	1
3	To determine the resistance of a galvanometer using Thomson's method	1
4	To study the response curve of a parallel LCR circuit and determine its (a) Anti-resonant frequency and (b) Quality factor Q	1
5	To investigate the motion of coupled oscillators	1
6	To study Lissajous Figures. Complete with CRO and Function Generator	1
7	Half Adder, Full Adder and 4-bit binary adder	1
8	To determine an unknown low resistance using Carey Foster's Bridge	1
9	Half Subtractor, Full Subtractor, Adder-Subtractor using Full Adder IC	1
10	To build Flip-Flop (RS, Clocked RS, D-Type and JK) circuits using NAND gates	1
11	To build JK Master-Slave flip-flop ICs	1
12	To build a 4-bit counter using D-type/JK flip-flop ICs and study timing diagram	1
13	To make a 4-bit shift register (serial and parallel) using D-type/JK flip-flop ICs	1
14	To design an astable multivibrator of given specifications using 555 timer	1
15	To design a monostable multivibrator of given specifications using 555 timer	1
16	To show the tunneling effect in tunnel diode using I-V characteristics	1
17	To study V-I characteristics of PN junction diode, and light emitting diode	1
18	Study of V-I & power curves of solar cells, and maximum power point and efficiency	1
19	To study the various biasing configurations of BJT for normal class A operation	1
20	To design a CE transistor amplifier of a given gain (mid-gain) using voltage divider bias	1
21	To study the frequency response of voltage gain of a RC-coupled transistor	1
22	To design a phase shift oscillator of given specifications using BJT	1
23	To study the Colpitt's oscillator	1
24	To design a digital to analog converter (DAC) of given specifications	1


Principal
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